

11046 US 5 PTO
10/008696

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10008696	FILING DATE 11/08/2001	CLASS 717 710	SUBCLASS 247 240	GAH 2185	EXAMINER Rinehart K Dang
----------------------	---------------------------	---------------------	------------------------	-------------	-----------------------------

**APPLICANTS: Reimer Jay; Hopkins Harland; Nouyen Tai; Luo Yi; McGonagle Kevin;
Jones Jason; Nguyen Duy; Smith Patrick;

2/8/11
2/8/2011

**CONTINUING DATA VERIFIED:

THIS APPLN CLAIMS BENEFIT OF 60/249,083 11/15/2000

BEST AVAILABLE COPY

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claims: 35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO TI-30105
Verified and Acknowledged Examiner's initials		
TITLE : Multicore DSP device having coupled subsystem memory buses for global DMA access U.S.DEP'T. OF COMM./PAT. & TM-PTO-435L(Rev. 12-54)		

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheets Drwg.	Figs. Drwg.
Primary Examiner		Print Fig.	
PREPARED FOR ISSUE		Application Examiner	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			
FILED WITH: <input type="checkbox"/> DISK (CRF) <input type="checkbox"/> CD-ROM (Attached in pocket on right inside flap)			

TERMINAL

DISCLAIMER